



MADANAPALLE INSTITUTE OF TECHNOLOGY & SCIENCE, MADANAPALLE – 517325 (A.P)
(UGC – AUTONOMOUS INSTITUTION)

(Affiliated to JNTUA, Ananthapuramu & Approved by AICTE, New Delhi)

M. Tech

Registration form for **M. Tech (VLSI Design and Embedded Systems) – II Year I Semester (R24)**

(For 2024 admitted batch)

(All the below details are mandatory and should be filled carefully)

Whether the Candidate is appearing for ☐ Regular Examinations ☐ Supplementary Examinations

Month & Year of Examination

H.T.No.

Name: (As per S.S.C Certificate)																			

Father's Name: (As per S.S.C Certificate)																			

Mother's Name: (As per S.S.C Certificate)																			

Date of Birth (As per SSC): (DD/MM/YYYY)									
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Contact No:									
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Tick [✓] the appropriate box

Gender		
	Male	Female

Physically Handicapped (PH)

Yes	No

Caste								
	SC	ST	BC-A	BC-B	BC-C	BC-D	BC-E	OC

Course for which registration is required: Tick [✓] the appropriate box

Professional Elective – V				Open Elective			
1	24VESP4M01	VLSI Interconnect	☐	1	24CSEP301	Advanced Data Visualization Techniques	☐
2	24VESP4M02	C-Based VLSI Design	☐	2	24CSEP302	Enterprise Cloud Computing	☐
3	24VESP4M03	Multi-Core Computer Architecture	☐	3	24MEP301	Automation and Robotics	☐
			☐				
			☐	1	24VESP702	Dissertation Phase I	☐

Certified that the above information is CORRECT and filled by me.

Signature of the Candidate



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**Hall Ticket
Duplicate**

M. Tech

H.T.No.

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Centre:

Madanapalle Institute of Technology & Science

Please affix your
latest passport
size – colour
photograph

1. Name of the Candidate:
2. Father's Name:
3. Month and Year of Examination:
4. Regular /Supplementary:

Tick [√] the appropriate box to register for a course

Professional Elective – V			
1	24VESP4M01	VLSI Interconnect	
2	24VESP4M02	C-Based VLSI Design	
3	24VESP4M03	Multi-Core Computer Architecture	

Open Elective			
1	24CSEP301	Advanced Data Visualization Techniques	
2	24CSEP302	Enterprise Cloud Computing	
3	24MEP301	Automation and Robotics	
1	24VESP702	Dissertation Phase I	

Signature of the Candidate

Controller of Examinations

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Instructions to Candidates

The candidate has to:

1. Display his/her ID card for verification.
2. The candidate has to confirm himself/herself that he/she does not possess any foreign material, electronic gadgets other than electronic calculator.
3. Candidates should stay in the examination hall at least half-an-hour from the commencement of the examination.
4. Be present in the examination hall 15 minutes before the time of commencement of examination.

No candidate will be allowed after the commencement of the examination.



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4. Regular /Supplementary:
5. **Tick [✓] the appropriate box to register for a course**

Professional Elective – V			
1	24VESP4M01	VLSI Interconnect	
2	24VESP4M02	C-Based VLSI Design	
3	24VESP4M03	Multi-Core Computer Architecture	

Open Elective			
1	24CSEP301	Advanced Data Visualization Techniques	
2	24CSEP302	Enterprise Cloud Computing	
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